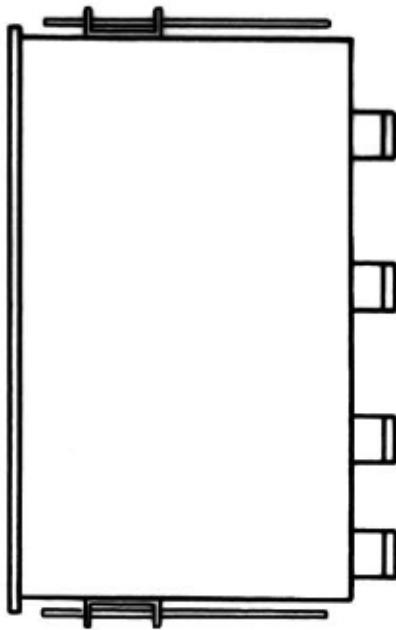
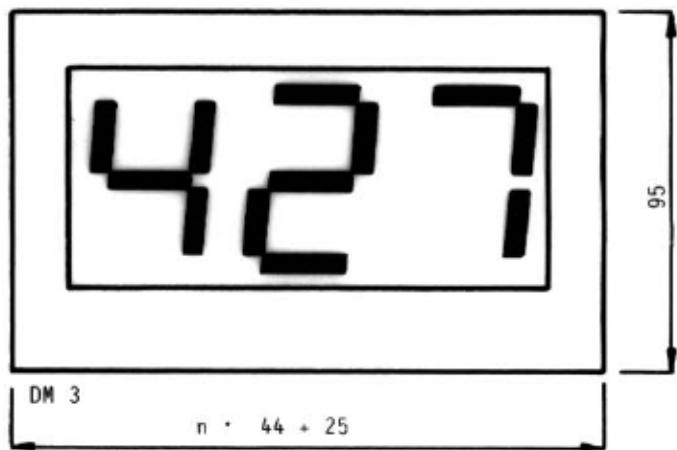
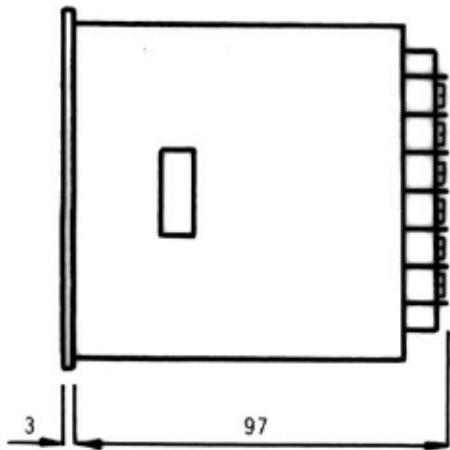


Maxiled

DM 50

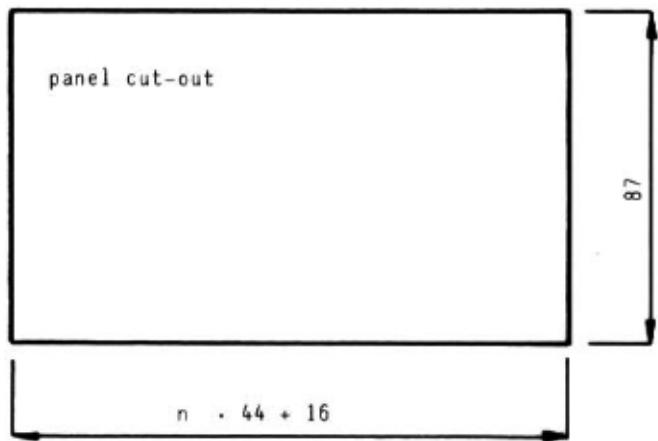


1 up to 6 digits display - for 24 Vdc logics

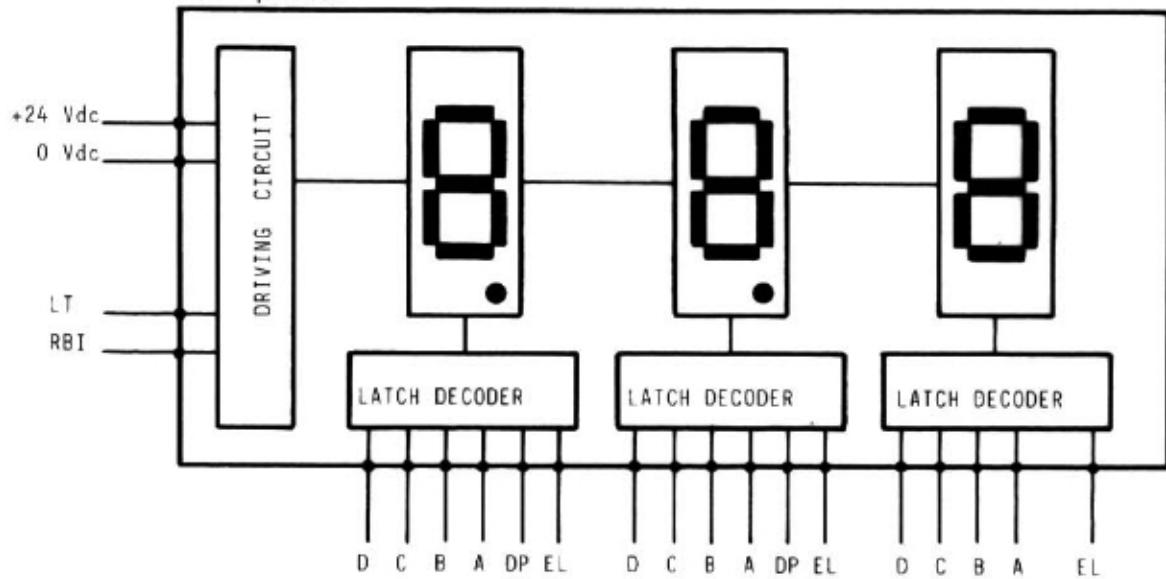


scale drawing 1 : 2

n = number of digits



DM 3 pin out



GENERAL SPECIFICATIONS

DM Giant Displays are numerical 7 segment LED displays properly suited to be driven by the 24V outputs of the programmable controllers (PLC). They are operated by the BCD code and the multiplex data transmission is enabled by the EL control line. All inputs are connected to +24Vdc (jumper PR) or ØVdc (jumper PS) via internal pull up or pull down resistors.

DM 50 Displays are available in six different models from one up to six digits.

TECHNICAL DATA (values referred to 1 digit)

Front panel colour	- black mat finishing
Light colour	- red
Digit size	- 50 mm.
Supply voltage Vdc	- 24 V \pm 10%
Supply current Idc	- 150 mA typ.
Operating temperature	- 0°C \pm 50°C
Connections	- screw terminals

INPUT DATA (DCBA; EL; RBI; LT)

Input voltage Vin ("0")	- min. 0 V \pm max. 4 V or open (with PS jumper)
Input voltage Vin ("1")	- min. 11 V \pm max. 24 V (with PS jumper)
Input voltage Vin ("0")	- min. 0 V \pm max. 4 V (with PR jumper)
Input voltage Vin ("1")	- min. 11 V \pm max. 24 V or open (with PR jumper)
Input current Iin ("0")	- 1 mA typ. (Vin = 2,7 V)
Input current Iin ("1")	- 9 mA typ. (Vin = 24 V)
Overvoltage protection	- up to \pm 60 Vpp - 10 m.sec. typ. lenght

NOTE: The decimal point DP must be controlled externally (DP on + 24 Vdc \rightarrow Decimal Point on).

TRUTH TABLE

INPUTS					DISPLAY			
D	C	B	A	EL	RBI	LT	0+9	\pm 1
0	0	0	0	0	1	1	0	\pm 1
0	0	0	1	0	1	1	1	1
0	0	1	0	0	1	1	2	
0	0	1	1	0	1	1	3	-1
0	1	0	0	0	1	1	4	
0	1	0	1	0	1	1	5	-
0	1	1	0	0	1	1	6	+
0	1	1	1	0	1	1	7	
1	0	0	0	0	1	1	8	
1	0	0	1	0	1	1	9	
X	X	X	X	1	1	1	stored	
X	X	X	X	X	0	1	blank	
X	X	X	X	X	X	0	8 (test)+1	

NOTE: With binary input $>$ "1001" the display will be blank.

"1" = high logic level

"0" = low logic level

"X" = "0" or "1"

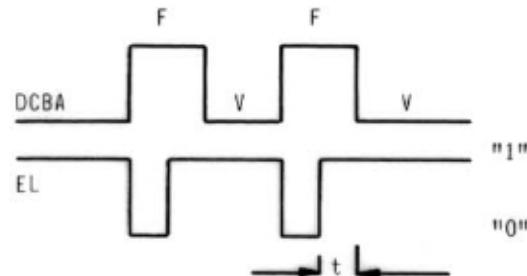
EL = Enable latch

RBI = Blanking input

LT = Lamp test

Multiplex data operation

timing diagram (1 digit)



V = changing data

t = 0.1 ms. min.

F = data cannot change

ORDERING CODE

SERIE

Display: 9; 99; 999; 9999; 99999; 999999; +19; +199; +1999; etc.

With pull down resistors = PS ; with pull up resistors = PR

Subject to change without notice.

DM50/R 999 / PS